# High-Intensity Airborne CMUT Transmitter Array With Beam Steering

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Abstract—A 2 × 2 high-intensity CMUT transmit array that is capable of two-dimensional beam steering is presented. The device uses an ac drive voltage at half the ultrasound frequency without any dc bias, enabling the usage of the entire gap height. The device is designed using a large signal equivalent model approach. A fabrication method that requires a single lithographic mask has been used. The fabricated devices are operated at 76 kHz to beam steer at various angles. An equivalent element pressure of 144 dB//20  $\mu$ Pa at the transducer surface was measured. The entire half-space can be steered without any sidelobes and the beam obtained from the array is in excellent agreement with the theoretical predictions. [2020-0253]

*Index Terms*—Airborne ultrasound, beam steering, CMUT, high intensity, transducer array, unbiased CMUT.

### I. INTRODUCTION

RRAYS in ultrasonic technology have been used due to their ability of electronic beam forming and steering at varying angles or depths. Ultrasonics in air provides a challenge in implementation because of high acoustic impedance mismatch and high attenuation. Capacitive micromachined ultrasonic transducers (CMUTs) are now technologically advanced to compete commercially with available piezoelectric ultrasonic transducers in applications requiring high acoustic intensity.

Novel ferro-electric materials and fabrication methodologies provide phased array applications with a pressure of 102 dB@0.2 m (All dB values in this work are referenced to 20  $\mu$ Pa *rms*) at 100 kHz using focused beamformed 32 elements linear array [1]. A 1-dimensional, 8-element flextensional ultrasonic transducer array operated at 30 kHz

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shows a phased array performance of 132 dB@0.3 m and 123 dB@1 m measured in the far field [2].

Airborne ultrasonic arrays are designed for two purposes: (i) as phased arrays to obtain a steered beam of sound in the far field, and (ii) to obtain a high ultrasonic intensity either to be exploited in the near field of the array or a fixed narrow beam. The element size in phased arrays must be small enough, compared to the wavelength of sound, so that the beam is steered in the space without limitations and sidelobes, whereas the latter can be implemented with larger element apertures.

CMUTs in airborne mode have been used for various applications [3]–[13]. Several works have been focused on highintensity directional ultrasound [14]–[17]. Currently, airborne ultrasonic devices are being used for ranging, wind sensing, gesture sensing, as well as obstacle sensing in smart cars and tactile displays [18]–[21]. There are phased array systems for narrowband airborne ultrasonic transmission that provide the capability of cascading several elements together for acoustic levitation, parametric loudspeaker, standing wave levitation, gravimetric sensor, fingerprint sensor, and ultrasonic haptic feedback [22]–[26].

All the reported arrays have different operating frequencies, relative sizes with respect to wavelength, beam-steering capabilities and employed technologies, which makes it difficult to make direct comparisons. The output pressure levels can be compared in terms of respective estimated pressure at the surface of the array [17] since the far field pressure is affected by the Rayleigh distance and beamwidth. For the array in [1], it is understood that array surface pressure is low, although 102 dB is obtained at 20 cm focus distance by beamforming 32 elements. The pressure at the surface of the array in [2] is estimated to be about 140-145 dB from the reported far field pressure levels.

Although CMUT transducers provide attractive features, they suffer from limited transmitted pressure when driven using *dc* voltage bias, due to collapse phenomena and the associated choice of membrane dimensions and gaps [27]. To overcome this constraint, in one study, piezo-transmitters and CMUT receivers have been successfully fabricated together that improve the transmit range and axial resolution of the system [28]. A CMUT design with an embossed pattern has been fabricated to improve output pressure in a liquid medium [29].

A large, 100-mm diameter, CMUT transmitter array generating a high transmitted pressure 107 dB@3 m (135 dB

1057-7157 © 2020 IEEE. Personal use is permitted, but republication/redistribution requires IEEE permission. See https://www.ieee.org/publications/rights/index.html for more information. referred to the surface) at 50 kHz, when biased at 380  $V_{dc}$  and driven using 200  $V_{ac}$ , has been reported for parametric array operation [17]. Airborne ultrasonic imaging based on the synthetic aperture technique that replaces the phased array by mechanically moving the elements is also reported [30].

In this work, we present design, production, and operation of a half-frequency driven unbiased CMUT array capable of beam steering at high intensity. When a dc bias is applied, the plate is depressed further at smaller drive levels and can collapse before it can reach the full swing. We demonstrate, both analytically and experimentally that an airborne CMUT transmitter array can be optimally designed to provide beam steering while providing a high output pressure normalized to the surface. The general physical requirements, such as element size and spacing, for better performance, is a wellstudied topic. The elastically linear operation requirement for CMUTs, however, imposes limitations on the element size [31].

Lumped element equivalent circuit-based model [32] was exploited to derive a CMUT transmitter array operated at zero bias. It is shown in [33] that very high radiated pressure can be obtained from a CMUT if it is driven unbiased, where the radiation plate vibration can span the entire gap height without collapsing.

In [34], the unbiased array operation is demonstrated by implementing beamforming and steering using appropriate phasing at half the frequency of transmission. The elements of the array are driven within the elastically linear range, where the generated acoustic intensity is low.

In this work, we extend the array operation to very highintensity transmission, where both beamforming and steering are done up to 144 dB surface pressure levels. It is shown that the required phasing for linear operation remains the same at the highest intensity level, although the dynamics of the radiation plates are no longer elastically linear. We report a design methodology for arrays that have very high-power transmission performance.

Fabrication inaccuracies such as a change in radius, plate thickness, or gap height may lead to discrepancies in frequency response and radiated pressures. On the other hand, the beamsteering fundamentally requires all elements in an array to perform similarly. In this paper, we show that beam steering can be achieved in the entire range by amplitude and phase compensation, despite these inaccuracies. We used our models to account for these deviations and henceforth demonstrate the optimally compensated beam steered CMUT array. Circuit model findings were verified using a set of measurements on several CMUT elements and arrays.

# II. CMUT CELL DESIGN

A CMUT transducer array can be designed and optimized using both finite element analysis (FEA) and equivalent circuit modeling. Using equivalent circuit models together with self and mutual radiation impedance yields accurate results which compare well with measurements even in large-signal airborne applications [32]–[36]. The equivalent circuit based approach provides an advantage over the FEA when simulating arrays with multiple cells and elements [37].

 $\begin{array}{c} 2x2 \text{ Array} \\ \hline E1 \text{ d} \\ \hline E2 \\ \hline a \\ \hline E3 \\ \hline \hline E4 \\ \hline \end{array} \begin{array}{c} Top \\ \hline Top \\ \hline \\ F_{RB} \\ \hline \\ \\ Silicon \\ \hline \\ Al_{2}O_{3} \\ \hline \\ \\ Bottom \\ Electrode \\ \hline \\ \\ Pyrex Substrate \\ \hline \end{array}$ 

Fig. 1. A cross-section of a single element in a  $2 \times 2$  CMUT array.



Fig. 2. A large-signal equivalent circuit model using spatial rms quantities for a single CMUT element.

## A. Single Cell Design

Fig. 1 shows a cross-sectional view of a typical CMUT element. Here, *a* denotes the radius of the radiation plate,  $t_g$  is the gap height between the radiation plate and bottom electrode,  $t_i$  denotes the thickness of the insulator layer,  $t_m$  is the thickness of the plate and  $F_{RB}$  denotes the static force being exerted on the plate. An acoustic wave can be generated in a CMUT element by applying an electrical voltage between the two electrodes.

The *dc* operation of a CMUT puts a limit on the amount of plate swing achievable for a given gap. With a *dc* bias, the plate is depressed at no excitation and the collapse phenomenon limits the swing amplitude. Consequently, only a portion of the available gap height can be utilized for plate swing in biased operation. The unbiased *ac* operation of the CMUT employs the terminal voltage at half the desired radial frequency,  $\omega$ , given as [33], [38]

$$V(t) = V_m \cos\left(\frac{\omega}{2}t\right) \tag{1}$$

Since the transduction force is proportional to the square of the potential difference between the terminals, the dynamic force on the plate is at the desired radial frequency  $\omega$ .

The large-signal equivalent circuit model [32] shown in Fig. 2 assumes that the plate is rigidly clamped over the edges. The left-hand side of the equivalent circuit represents the electrical side of the circuit, with  $C_{o}$  being the capacitance of the undeflected plate.  $i_C$  represents the additional current when the capacitor value is changed because of deflection.  $i_V$  is the current arising from the velocity in the mechanical side. The right-hand side is the mechanical section, with  $C_{Rm}$  and  $L_{Rm}$  representing plate compliance and mass of the plate respectively.  $v_R$  is spatial rms particle velocity and  $f_R$  is a nonlinear voltage-controlled voltage source generating the transduction force.  $Z_{RR}$  is the radiation impedance at the acoustic port.  $f_{RO}$  is the transmitted force generated at the acoustic terminal while  $F_{RB}$  is the force caused by the static ambient pressure. The model includes the effects of dielectric loss  $(R_P)$  and the frictional loss  $(r_{loss})$  and the

loss to backing impedance  $(Z_b)$  [33], [39].  $C_P$  stands for the parasitic capacitance of the cell.

A thin clamped plate displacement profile is given as [40],

$$x(r,t) = x_p(t) \left(1 - \frac{r^2}{a^2}\right)^2$$
 for  $r \le a$  (2)

when a uniformly distributed force acts on its surface. Here r is the radial position and  $x_p$  is the center displacement of the plate towards the gap. This profile function is maintained as long as the plate deflection is low and the vibration is elastically linear. As the deflection amplitude increased, the profile gradually deviates from (2). It is accepted that the plate motion is in elastic linear range if the center displacement is less than 20% of the thickness of the plate [41].

It is shown in [31] that CMUT resonance remains in the vicinity of the mechanical resonance frequency determined by  $C_{Rm}$  and  $L_{Rm}$ , when driven unbiased. This resonance frequency prevails even at very high dynamic displacement amplitudes, as long as the static center deflection of the plate due to atmospheric pressure is within the linear elastic range. Duffing effect [42] on the resonance frequency due to the stiffening of plate material is overwhelmed by the increased nonlinearity in the dynamic transduction force when the vibration amplitude spans the entire gap.

The quality factor of the mechanical section,  $Q_m$ , for a single lossless CMUT element is given as

$$Q_m = \frac{\omega_r L_{Rm} + X_{RR} (k_r a)}{R_{RR} (k_r a)} = \frac{k_r a}{R_1 (k_r a)} \frac{t_m}{a} \frac{\rho_m}{\rho_0} + \frac{X_1 (k_r a)}{R_1 (k_r a)}$$
(3)

where  $t_m/a$  is obtained from the resonance condition as,

$$\frac{t_m}{a} = (k_r a) c_0 \sqrt{\frac{9(1 - \sigma^2)\rho_m}{80Y_0}}$$
(4)

in terms of velocity of sound in air,  $c_0$ , and the material properties. Here,  $k_r = 2\pi/\lambda$ , is the wavenumber in air at the resonance frequency,  $f_r$ , and  $\rho_m/\rho_0$  is the density ratio of plate material to air.  $R_1$  and  $X_1$  are the normalized real and imaginary parts of the radiation impedance,  $R_{RR}(k_ra) + jX_{RR}(k_ra)$ , of the transducer, respectively.  $Y_0$  and  $\sigma$  are the Young's modulus and Poisson's ratio of the radiation plate, respectively.

For a silicon plate, (3) predicts a minimum quality factor at  $k_r a \approx 0.5$ , where the transducer bandwidth is maximum. This value is not usable, since the corresponding  $a/t_m$  ratio found from (4) is very large (> 130), requiring a very thin plate. Such thin plates suffer stiffening in CMUTs with a vacuum gap when subjected to atmospheric pressure. Geometrical elastic nonlinearity occurs due to excessive static center displacement. In [43], it is shown that the maximum  $a/t_m$  ratio that a silicon plate can have is about 35 for entirely linear (elastic) operation in air when no bias is applied.  $a/t_m < 35$  corresponds to  $k_r a > 1.95$  for a silicon plate.

In order to sample the entire Fourier half-space unambiguously, the center-to-center inter-element spacing must be less than half a wavelength [16], [44]. This requires  $k_r a < 1.57$ . If the element size is larger and consequently the spacing is more than half a wavelength, the grating lobes of the array

TABLE I MATERIAL PROPERTIES AND DIMENSIONS

Physical Property	Symbol	Value	Units
Young's Modulus (Si)	$Y_0$	148	GPa
Poisson's Ratio (Si)	$\sigma$	0.17	
Density (Si)	$ ho_m$	2370	kg/m <sup>3</sup>
Dielectric constant (Al <sub>2</sub> O <sub>3</sub> )	$\varepsilon_r$	9.7	
Plate Thickness (Si)	$t_m$	40	μm
Insulator Thickness (Al <sub>2</sub> O <sub>3</sub> )	$t_i$	100	nm
Gap Height	$t_g$	10	μm
Plate Radius	ā	1.4	mm
Center-to-center Element Pitch	d	3	mm

emerges and gradually become larger [45], [46]. This effect is very significant if the spacing is close to a wavelength. However, grating lobes remain small, comparable to sidelobe levels, for element spacing up to about 90% of the wavelength. Often, arrays are steered in a smaller sector such as  $-\pi/3to + \pi/3$ rather than the entire half space,  $-\pi/2to + \pi/2$ , in which case the element spacing can be larger for unambiguous steering performance within the sector [46]. Considering both of these issues, designing an array having elements with  $k_r a \approx 2$ secures elastic linear operation for low amplitude vibrations and the resulting element spacing of 65% of a wavelength maintains beam steering in a large sector.

Most of the airborne ultrasonic arrays operate between 30-100 kHz. We designed our CMUT cells to operate in 70-80 kHz band. The attenuation in air is slightly more than 2.2 dB/m at this frequency and at SAP (Standard Atmospheric Pressure, 101 kPa) and  $20^{\circ}$ C [47].

For a silicon plate with  $k_r a \approx 2.0$  and  $a/t_m \approx 35$ , a mechanical resonance frequency of about 77.6 kHz is obtained by  $t_m = 40 \mu m$  and a = 1.4 mm using,

$$\omega_r = \frac{t_m}{a^2} \sqrt{\frac{80Y_o}{9(1-\sigma^2)\rho_m}} \tag{5}$$

With these dimensions, using the material properties given in Table I, the center deflection due to atmospheric pressure  $(X_p)$  must be less than 8  $\mu$ m so that elastic linearity is maintained [41], i.e.,  $X_P/t_m < 0.2$ .

For  $X_p = 6.8 \ \mu \text{m}$  at ambient pressure, we choose an equivalent gap height of  $t_{ge} = 10 \ \mu \text{m}$ . For these dimensions, driving peak voltage amplitude of approximately 100 V will yield maximum swing without the plate hitting the substrate. The CMUT with these chosen parameters has a collapse voltage in air ( $V_c$ ) of 250 V and in vacuum ( $V_r$ ) of 820 V. The other equivalent circuit model parameters [32] are  $C_0 = 5.4 \text{ pF}$ ,  $L_{Rm} = 0.58 \ \mu \text{H}$ , and  $C_{Rm} = 7.2 \ \mu \text{F}$ .

# B. Array Design

For a CMUT array design, in general, it is essential to study mutual or self-impedance of elements in an array [37]. The elements in an array are coupled at the acoustic terminals through an impedance matrix. The equivalent circuit model (Fig. 2) is combined with an appropriate impedance matrix. For a  $2 \times 2$  array, the equivalent circuit model was simulated



Fig. 3. Cross-section of the process flow for CMUT fabrication: (a) Cr deposition on Pyrex wafer; (b) PR spin coat; (c) Photolithography; (d) PR development; (e) Cr etch; (f) BOE etch to open cavities; (g) Metal Deposition; (h) Metal Liftoff; (i) Initial SOI wafer; (j) ALD Alumina deposition; (k) Anodic wafer bonding of SOI and Pyrex wafer; (l) Handle and BOX layer etch.

in ADS (Advanced Digital Systems, Keysight Technologies, Santa Rosa, CA), terminated by this impedance matrix [37]. The mutual impedance effects on the array performance were found to be insignificant. This is expected since the acoustic impedance of air is very small compared to the mechanical branch impedance of the CMUT cells with thicker plates and narrower bandwidth.

## III. CMUT FABRICATION

## A. Microfabrication of CMUT Arrays

CMUT devices can be microfabricated using either wafer bonding or surface micromachining [9], [48], [49]. In our study, the wafer bonding approach is considered more advantageous for the production of the CMUTs that have large diameters and deeper gaps.

#### The process flow is as follows:

1) Pyrex Substrate Processing: A 30 nm thick layer of chromium (Cr) is blanket deposited on 100-mm diameter, 500  $\mu$ m thick Pyrex using e-beam evaporation (Fig. 3a). Cr layer is used as an adhesion layer between the photoresists and Pyrex surface and as a hard mask for a subsequent extended wet buffered oxide etch (BOE 7:1).

Standard photolithography for the only mask is implemented by using 8  $\mu$ m thick photoresist (AZ4562,



Fig. 4. (a) Optical Image of a single CMUT element imaged through transparent Pyrex substrate, (b) Cross-sectional SEM of SOI wafer with close up of thin layers in the in-picture.

MicroChemicals GmbH, Germany) (Fig. 3b and c). After photoresist development and hard bake (Fig. 3d), the Pyrex wafer is dipped into wet Cr etchant (TechniEtch Cr01, Micro-Chemicals GmbH, Germany) to create openings on the Cr film (Fig. 3e). The wafer is then immersed into BOE solution for 8.5 hours to achieve an etch depth of 10.25  $\mu$ m to form the gap of the CMUT array devices (Fig. 3f). Long wet etch process extended radius *a* by 25±2  $\mu$ m that was verified using stylus profilometer (Dektak XT, Bruker Nano Surfaces Division, Tucson, AZ, USA) and optical measurements.

A metal stack of Ti/Pt/Au (100 nm/100 nm/50 nm) is deposited using e-beam evaporation (Fig. 3g). Ti and Pt film combination is used as a getter material combination [50] during anodic wafer bonding.

A piranha solution is used to etch the hardened photoresist for lifting off the Ti/Pt/Au layers. The remaining Ti/Pt/Au metal stack inside the etched gap regions acts as the bottom electrode of the CMUT devices (Fig. 3h). Cr etchant is then used to remove Cr from the entire Pyrex wafer surface. Fig. 4a shows an optical image of a single CMUT element imaged through the transparent Pyrex substrate.

2) Silicon-on-Insulator (SOI) substrate processing: 100-mm diameter, highly doped, SOI (P-type (B), device resistivity <0.0015  $\Omega$ cm, Ultrasil Corp, USA) wafers with 40  $\mu$ m  $\pm$  0.5  $\mu$ m thickness of Si device layer, 2  $\mu$ m  $\pm$ 5% thickness of buried oxide (BOX) layer and 385  $\mu$ m  $\pm$ 15  $\mu$ m thickness of handle layer (Fig. 3i) are chosen. 100 nm thick alumina layer (Al<sub>2</sub>O<sub>3</sub>-Insulator layer) is deposited on the device side of the SOI wafer using a thermal atomic layer deposition (ALD- Savannah, Cambridge Nanotech Inc., USA) process (Fig. 3j). Fig. 4b shows a cross-sectional SEM image of the SOI wafer.

To check the surface quality, an AFM (MFP-3D, Oxford Instruments Asylum Research Inc. Santa Barbara, CA, USA) measurement on the alumina surface is performed. The measurement showed that the average surface roughness of the alumina coated surface is 0.2 nm. Pyrex and SOI wafers are bonded to each other by using an anodic wafer bonding service (EV Group E. Thallner GmbH, Austria) (Fig. 3k).

3) Post-Processing: After the bonding process, all the CMUT gaps, and a significant portion of the metal stack used for electrical wiring and base electrode formation, is under the ALD alumina coated SOI wafer. The electrical connection pads of the individual CMUT array elements are kept



Fig. 5. (a) CMUT array mounted onto a rotating stage. The stage as well as the microphone mounting stand are covered in ARC to absorb reflections. Both microphone and substrate are placed farther away to avoid reflecting planes. (b) Close up image of a CMUT array displaying depressed CMUT membranes. (c) Stylus profile of a statically depressed CMUT membrane.



Fig. 6. Impedance measurement of a single CMUT with frequency and bias sweep.

outside the SOI wafer to allow external electrical connections (Fig 5). As the gaps are still at atmospheric pressure, the channels inside the Pyrex wafers need to be vacuum sealed (Fig. 3k).

The entrances of the wiring channels are manually sealed using a low viscosity epoxy resin (Biresin CR120 epoxy-resin & CH120 Hardener, Sika, Baar, Switzerland). The wafers are then placed into a vacuum chamber for over 15 hours to remove air from the cavities while partially curing the low



Fig. 7. Measured and model-predicted response for conductance and susceptance curves of a single CMUT element.



Fig. 8. Block diagram for pressure measurements of a CMUT transmit array. CMUT array is placed in a rigid baffle. Measurements are performed in x - z plane. In far field,  $\theta$  for an element becomes congruent to that of an array.

viscosity epoxy resin. After the gaps beneath the radiation plates are vacuumed, the wafers are immediately transferred to an oven for hard curing of the epoxy at 120°C for 6 hours (Fig. 31).

A donut-shaped shadow mask is attached to the top of the Pyrex substrate to mask the exposed electrodes, leaving only the handle layer of the SOI wafer available for Si reactive ion etching (RIE). SF<sub>6</sub> and Ar based RIE recipe was used in an ICP chamber (ICP615, Surface Technology Systems, UK) to isotropically dry etch the handle layer. After removal of the handle layer, 2  $\mu$ m thick BOX layer is wet etched using BOE to reveal the device layer of the SOI wafer which forms the vibrating plates of the CMUT array devices (Fig. 31). Stylus profilometer measurements are carried out on all elements and 7  $\mu$ m depression ( $X_P$ ), at the central point of each CMUT plate across the wafer is observed (Fig. 5 b-c). This measurement is consistent with our calculations, which is an indication of a good vacuum seal.

A PCB is used to take out the electrical connections from the Pyrex wafer (Fig. 5a). Among four of the  $2 \times 2$  CMUT arrays fabricated on a wafer, all elements in two of the CMUT arrays were able to generate pressure, while the other two



Fig. 9. Calculated and measured directivity (normalized) for a  $2 \times 2$  CMUT array in the x-z plane. (a) beam steered at 105°, (b) 90° and (c) 30° with respect to the surface of CMUT array.

CMUT arrays had at least one element non-operational after the bonding process.

#### B. Design Validation

The CMUT cells are characterized using an impedance analyzer (HP4194A, Hewlett-Packard, Palo Alto, CA). The measurement is performed by sweeping bias voltage from -20 V to 20 V at an *ac* drive of 0.5 V in a long integration mode. Fig. 6 shows an impedance measurement of a CMUT element with symmetry around zero bias. This behavior indicates that the amount of charging on the CMUT radiation plate is insignificant [51]. The unbiased operation is not possible if the insulator layer is charged. On the other hand, the unbiased operation does not induce charging in the insulator, since the electric field is alternating. A slightly lower resonant frequency and an increased bandwidth in comparison to the model's predicted response is observed. A similar conductance response with a 2% dispersion in resonance frequency is observed among 35 out of 45 elements, which were operational after the fabrication.

The variations in the radius of the gap due to extended BOE etch lowers the resonance frequency by 1.8% on the average with some dispersion. Furthermore, the vibration of the radiation plates excites waves in the silicon plate at the clamped edge, and in the substrate [33]. This energy manifests itself as loss and contributes to the enlargement of the bandwidth. Energy loss due to trapped air in the gap is not expected to be significant. The static depression of the plate in each cell, depicted in Fig. 5(c), are in agreement with the design requirement, which verifies that the vacuum seal is good.

The calculated quality factor together with measured conductance peaks can be used to account for losses in the elements [33]. With  $r_{loss} = 2.21\pi a^2 \rho_0 c_0$ ,  $C_P = 1.76C_0$ , a loss tangent of 0.00014, and a very large  $Z_B$ , we were able to match the model results with the measurement results [33] closely as shown in Fig. 7.

FEA for a single CMUT cell was also performed to determine the resonance frequency (Trans126 is used as single CMUT transducer material to perform modal analysis to extract  $f_r$  in ANSYS v.14.5, ANSYS Inc., Canonsburg, PA). The obtained response is very close to circuit modeling and measured CMUTs (Table II).

TABLE II Resonance Frequency Obtained via Circuit Modelling, FEA and Measurement

	Circuit Model	FEA	Measurement (Avg.)
Resonance Frequency $(f_r)$	77.6 kHz	77 kHz	76 kHz

## IV. PERFORMANCE EVALUATION

## A. Pressure and Directivity Calculation

Pressure field, p, for a clamped plate CMUT array, with N elements, located on an infinite rigid baffle, can be calculated using the equation [37]

$$p(r,\theta,\phi) = j \frac{\rho_m c_0 k_r \pi a^2}{2\pi} D(\theta) \sum_{i=1}^N U_{R_i} \frac{e^{-jk_r r_i}}{r_i}$$
(6)

where,

$$D(\theta) = \frac{48 J_3(k_r a \sin \theta)}{(k_r a \sin \theta)^3}$$
(7)

is the pressure directivity pattern of a CMUT cell [52]. Here  $J_3$  is the Bessel function of the 3<sup>rd</sup> kind,  $U_{Ri}$  is the *rms* velocity phasor at the surface of the *i*<sup>th</sup> cell and  $r_i$  is the radial distance of the element to the observation point in a medium. The pressure for a single element measured at a specific distance can be used to calculate the pressure at the surface of the transducer.

As the measurements are taken in the *x*-*z* plane,  $\varphi$  becomes zero. In far field of the array,  $\theta$ , for a single element CMUT becomes congruent to that of an array (Fig. 8). For a 2 × 2 array, directivity pattern,  $D_p(\theta)$ , can be written as [53],

$$D_p(\theta) = \frac{\sin\{(k_r d) \sin \theta\}}{4\sin\{(k_r d/2) \sin \theta\}}$$
(8)

Dashed lines in Fig. 9 show normalized directivity patterns calculated using the design parameters in Table I.

### B. Array Compensation and Measurements

Due to the dispersion in resonance frequencies, not all of the array elements can be operated at the same frequency to obtain maximum pressure. For beam steering, it is crucial to have the same amount of pressure with controlled phase radiated from each element in an array. Hence a compensation is necessary



Fig. 10. Calculated normalized peak center displacement deflection using circuit model at  $V_m/V_r = 0.06$  for different elements of an array under SAP. To equalize the displacement amplitude for an array, the drive voltages for higher displacement amplitude elements at 76 kHz need to be reduced.

for both phase and amplitude of the output pressure of CMUT elements in an array. Equalization of displacement amplitude is possible if each of the elements is driven using a different drive voltage with an appropriate phase (Fig. 10).

A set of measurements are performed to measure rms pressure of each CMUT element to produce a calibration curve. The CMUT elements are individually driven using a multichannel digital-to-analog (D/A) converter (PXI-6733 card, NI PXIe-1073 Chassis, National Instruments, USA) in pulsed configuration (Fig. 8) with 4 ms long pulse and 1 s repetition rate. A measurement microphone (pressure-field microphone, B&K 4138, Bruel and Kjaer, Naerum, Denmark) is used to measure the radiated pressure. The dynamic range (3% THD) of this microphone is between 52.2 dB and 168 dB SPL [54], [55], the upper limit of which is well above the measured pressure levels in this work. The microphone is mounted on a preamplifier (B&K 2633, Bruel and Kjaer, Naerum, Denmark) using an adaptor (B&K UA 160, Bruel and Kjaer, Naerum, Denmark) and placed at the principal axis of the array at 15 cm distance. The microphone output is fed to a low noise amplifier (10-100 kHz bandpass filter with 6 dB roll-off, SR560, Stanford Research Systems, Sunnyvale, CA). The measurements were recorded in a laboratory environment where the relative humidity of 52% and temperature of 20°C was observed throughout the whole measurement process. The output pressure is measured separately using D/A, spectrum analyzer (HP8590l, Hewlett-Packard, Palo Alto, CA), and oscilloscope (DSO1002A, Agilent, Keysight Technologies, USA). The *rms* pressure for a time signal obtained using an oscilloscope is estimated from the amplitude at the latter part of the long pulse waveform after the transients are decayed (Fig. 11).

To compensate for phase variation between the elements, we excited two elements simultaneously at the same frequency and at their respective drive voltages required for a given pressure amplitude. The output pressure is maximized as one of the element's phase is adjusted (up to  $0.16\pi$  radians). The element



Fig. 11. A long pulse transmitted using a  $2 \times 2$  CMUT array at 76 kHz.



Fig. 12. Mean of pressure measurements at a distance of 15cm for a  $2 \times 2$  compensated array and its elements driven at 75.8 kHz, 76 kHz, and 77.6 kHz on the array axis obtained by a measurement microphone. The parallel bars show the standard deviation of pressure variation at different frequencies, which is very low below 80V.

pressure levels are used as a calibration to obtain equalized element output pressures necessary for compensation of array. In Fig. 12, the horizontal axis is respective drive voltage amplitudes for each element in element pressure measurements. In array mode, the horizontal axis represents drive voltage amplitude of E2 for compensated array pressure measurement while other elements in array pressure measurement are driven by their respective compensated voltage amplitude and phases. By matching each element pressure and phases in this manner, we were able to obtain an array pressure that is 11 dB higher than its least sensitive element of the four-element array.

#### C. Beam Steering a CMUT Array

For beam steering, the required excitation phase delay was calculated using a phased array geometry [56] for each of the elements in the array. In this study, elements were driven with a phase delay to steer the beam at  $30^{\circ}$ ,  $90^{\circ}$ , and  $105^{\circ}$ .



Fig. 13. Measurement setup for an airborne CMUT array for beam steering measurements. The distance from the microphone to the CMUT is 15 cm. All surrounding surfaces around the setup are covered with anti-reflective coatings to avoid reflections during the measurements.

TABLE III Comparison Between Selected Airborne Ultrasonic Phased Array Performances

Ref.	Туре	Primary functionality	Pressure	f <sub>r</sub> (kHz)	Drive (V)	Aperture (mm <sup>2</sup> ) Elements (N)
[1]	Ferro- electric	Beam steering	102 dB@ 20 cm focus <sup>a</sup>	100	400 V <sub>ac</sub>	109x34 32 element
[2]	Piezo- electric	Beam steering	132 dB <sup>b</sup> @30 cm	30	4 V	70x54
						8 element
			135 dB		380 V <sub>de</sub>	2 quadrants of 100 mm wafer <sup>c</sup>
[17]	CMUT Parametric array	surface pressure	50	200 V <sub>ac</sub>	35 of 71 elements	
This Study	CMUT	Beam	144 dB surface pressure	77	$0 \; V_{dc}$	6x6
					100 V <sub>ac</sub>	4 elements

<sup>a</sup> Near field measurement made using all elements driven together at focus.

<sup>b</sup> 140-145 dB estimated surface pressure.

<sup>c</sup> Element pitch is not reported making it difficult to calculate the actual aperture.

The CMUT array is operated in a continuous wave excitation mode. A lock-in amplifier (SR850, Stanford Research Systems, Sunnyvale, CA) is used with a time constant of 100 ms and roll-off of 12 dB/octave. A reference signal of similar amplitude and frequency is also provided to the setup. A fur is used at all surrounding reflective surfaces to reduce acoustic interference (Fig. 5). The CMUT wafer is erected vertically on top of a 360° stage (Fig. 13). The stage is manually rotated from 10° to 170° with 3° steps.

Dotted lines in Fig. 9 show normalized directivity pressure measurement at 76 kHz. The beam pattern was symmetric on the central axis. For beam steered at 30°, 90° and 105°, the calculations are consistent with the measured beam pattern within measurement tolerances.

## D. Discussions

Various airborne ultrasonic transmitting transducers capable of producing high pressure or beamforming and steering capability are summarized in Table III. This study presents a CMUT array that is capable of producing high pressure (144 dB at the surface of the transducer), which is higher than the pressures reported in the literature. Furthermore, the compensated array is beam steered in a precise manner. The compensation requirement remains constant for the entire drive range, which makes the array usable in applications where beam steering is required at high-pressure levels. In comparison to our design, other devices are driven at higher drive voltages.

#### V. CONCLUSION

We presented the design, microfabrication, and measurement of an airborne CMUT transmit array that produces high intensity, beam steerable ultrasound. The CMUTs were produced using an integrated fabrication method that involved a single lithographic process.

We achieved high transmission pressure using only *ac* drive, as we drive the CMUTs at half the operation frequency. We demonstrated that we can predict the performance of fabricated CMUTs. CMUT elements operate such that the plate movement covers the entire gap, hence providing the maximum pressure. For a single CMUT element, we experimentally obtained a maximum pressure of 144 dB at the surface of the transducer. A  $2 \times 2$  compensated CMUT array produced 11 dB higher pressure than the least sensitive element of the CMUT array.

Fabrication inaccuracies are inevitable in a multiuser cleanroom facility. Dry etching may be used to generate Pyrex cavities to lower variation in lateral dimensions. Making the fabrication process more efficient can reduce the discrepancies between the elements, which both reduces the compensation requirements and increases the amount of radiated pressure of an array. With the processes optimized for industrial fabrication facilities, the elements of the array will be almost identical, in which case the same amount of pressure can be produced using 40 V amplitude. Employing a more efficient electrical routing on the wafer, and usage of smaller die spaces with narrower or shorter electrode connections, can help to reduce the parasitic capacitance. A microfabrication process can be used to produce larger arrays that can be operated in unison with cascaded phased array drivers.

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