

On-Chip-Integrated Nanowire Device Platform With Controllable Nanogap for Manipulation, Capturing, and Electrical Characterization of Nanoparticles

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Abstract—We propose and demonstrate nanowire (NW) device platforms on-chip integrated using electric-field-assisted self-assembly. This platform integrates from nanoprobe to microprobes, and conveniently allows for on-chip manipulation, capturing, and electrical characterization of nanoparticles (NPs). Synthesizing segmented (Au–Ag–Au) NWs and aligning them across predefined microelectrode arrays under ac electric field, we controllably form nanogaps between the self-aligned end (Au) segments by selectively removing the middle (Ag) segments. We precisely control and tune the size of this middle section for nanogap formation in the synthesis process. Using electric field across nanogaps between these nanoprobe, we capture NPs to electrically address and probe them at the nanoscale. This approach holds great promise for the construction of single NP devices with electrical nanoprobe contacts.

Index Terms—Dielectrophoresis, electric-field-assisted self-assembly, nanocrystals (NCs), nanogap, nanoparticle (NP), nanowire (NW).

I. INTRODUCTION

ONE of the major challenges in nanofabrication commonly arises from the necessity to integrate nanostructures (e.g., nanoparticles, NPs) on the same chip with microcomponents (e.g., microelectrodes) that are orders of magnitude larger in size. For example, in order to make electrical contacts to colloidally synthesized NPs (typically 1–100 nm in size) by integrating them with microelectrodes (typically in the few micrometers range on the critical side), a very large size mismatch that

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easily ranges from 1:10 to 1:10 000 is required to be handled for successful nano-to-micro integration. This necessitates the ability to manipulate and integrate NPs with a sufficient level of precision on the microchip. In this study, to provide a convenient solution to this challenging problem, we proposed and demonstrated for the first time an on-chip-integrated nanowire (NW) device platform that features controllable nanogap formation in its architecture for capturing and electrical characterization of NPs in the gap, all fully integrated on the same microchip.

Pohl extensively investigated electric field assistance to align and separate various particles, a process also known as dielectrophoresis (DEP) [1]. Liu *et al.* previously studied gap formation relying on deposition and etching of Ag segment in the middle of Au NWs that are drop-cast on contact pads [2]. In previous work, Au NWs have also been successfully aligned on electrode arrays by using DEP [3]–[5]. Furthermore, metal NP manipulation has been achieved through DEP [6]–[9]. However, to date, there is no research work reported for fabrication and use of electric field-assisted, self-assembled, segmented NWs with controllable-size nanogaps to capture and electrically probe NPs.

Our approach relies on the use of dielectrophoretic self-assembly of our segmented NWs to construct a nanoscale device platform. One of the key benefits in this approach is that the aligned NWs clamped in the electrodes automatically provide electrical contacts to the captured NPs to allow for electrical probing at the nanoscale. This enables convenient full integration from NPs to NWs to microelectrodes to macroprobes on a single chip, spanning a size range of more than six orders of magnitude (Fig. 1).

II. THEORETICAL BACKGROUND

DEP stems from spatially nonuniform electric field distribution, where the induced dipole moment is translated into a net force on polarized particles. This can be used to manipulate particles, also at the nanometer scale. Due to this field gradient, the dielectrophoretic forces are generated as a result of the interaction of the applied electrical field with the induced charges, which are not equal on each side of the particles. This inhomogeneous nature of the electric field distribution causes a nonzero net force. The dielectrophoretic force strongly depends on the oscillation frequency of the applied nonuniform electric field, the complex permittivity of the host medium and the particles, and the particle size.

For example, a spherical particle of radius r with a permittivity of ε_p , in a host medium with the permittivity of ε_m , the

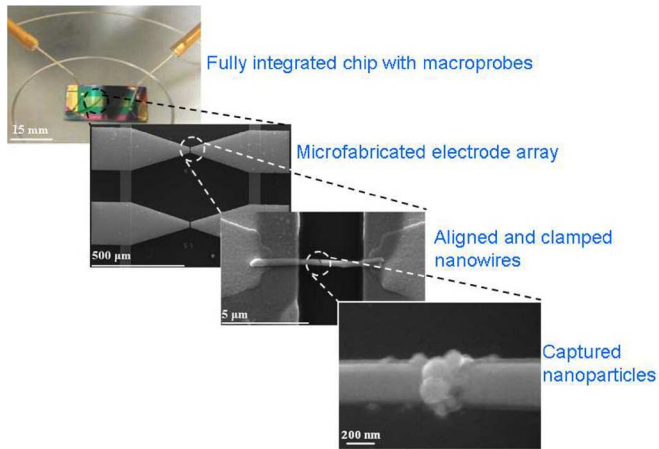


Fig. 1. On-chip-integrated NW device platform that integrates from macroprobes to microelectrodes and to nanoprobes for electrical characterization of captured NPs.

dielectrophoretic force is given by

$$F_{\text{DEP}} = 2\pi r^3 \varepsilon_m \text{Re} [K(\omega)] \nabla E_{\text{rms}}^2 \quad (1)$$

where r is the particle radius, ω is the angular frequency, E_{rms} is the root-mean-square electric field, and $K(\omega)$ is the Clausius–Mossotti function, which depends on the complex permittivity of the spherical particle and the medium by

$$K(\omega) = \frac{\varepsilon_p^* - \varepsilon_m^*}{\varepsilon_p^* + 2\varepsilon_m^*} \quad (2)$$

where $\varepsilon^* = \varepsilon - (j\sigma/\omega)$ is the complex permittivity.

Clausius–Mossotti factor defines the optimum frequency range to use DEP, which is also dependent on the dielectric properties and conductivity of the medium and particles. If the real part of this value is positive, then positive DEP takes place, for which the net force is directed toward the high field gradient and the particles move to high field gradients.

III. FABRICATION

A. Electrode Array Fabrication

We fabricate large arrays of microelectrodes in parallel configuration on silicon substrates with different architectural designs using standard microfabrication techniques including photolithography, metal deposition, liftoff, and plasma-enhanced chemical vapor deposition (PECVD). On a silicon substrate, we start with depositing silicon oxide (Si_xO_y) of 500 nm in thickness with a rate of 28 nm/s at 12 W at 250 °C (or sometimes alternatively, silicon nitride (Si_xN_y) instead of silicon oxide) as the isolation layer using PECVD to decrease the leakage current, to allow for achieving higher electric field buildup between upper electrodes during dielectrophoretic alignment, and to reduce noise level during I – V characterization while probing the electrodes. On the top of this dielectric layer, using photolithography, metal deposition, and liftoff, we define and pattern the first metal layer made of Au (bottom electrodes) that provides an electrical path to upper electrodes by conducting current through ac coupling. This bottom metal layer consists of 20-nm-thick Ti,

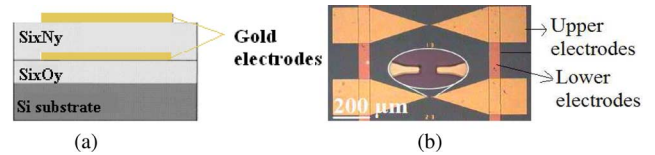


Fig. 2. (a) Cross-sectional sketch our device and (b) top view of optical microscopy image of two electrode pairs along with a zoom in of the tips of the upper electrodes.

100-nm-thick Au, and 20-nm-thick Ti. Titanium is used for adhesion layer between gold and Si_xN_y . Subsequently, we deposit an adjacent silicon nitride layer of 200 nm in thickness at a rate of 8 nm/s at 12 W at 250 °C. This serves as the dielectric layer for capacitive coupling between the lower electrodes and the upper electrodes. This dielectric layer also prevents electrical shortage of these electrodes to metallic NWs during the assembly process and enables characterization of each NW individually. Finally, we define and pattern the upper gold microelectrodes, which consist of 10-nm-thick Ti and 100-nm-thick Au layers on the silicon nitride layer using the same fabrication techniques as the first layer [Fig. 2 (a) and (b)].

The separation between the tips of our electrode fingers are designed to be 5–6 μm long. The large contact pads and the lower electrodes are designed to achieve the desired level of capacitive coupling between top and bottom metal layers. The large contact pads are set to be 800 μm \times 800 μm and the width of the lower electrodes is 60 μm . These parameters are designed to provide reasonable levels of electric field between the fingers of the upper electrodes for field-assisted self-assembly of NWs. For that, the potential drop between the alignment electrodes is maximized increasing the coupling area between the lower and upper electrodes when the voltage is applied to the large contact pads. Si_xN_y is chosen as the intermediate dielectric layer between the two electrodes for its higher dielectric constant of 7.5 compared to that of Si_xO_y , 3.9, which is another factor that increases the potential between the alignment electrodes on the chip.

B. Segmented Nanowire Synthesis

Using electrodeposition, we synthesize Au–Ag–Au segmented NWs in porous circular membranes made of aluminum oxide (Whatman Anodisc) employed as the hard template [10]–[12]. These membranes are 21 mm in diameter and 60 μm thick with a pore size of 200–300 nm and a pore density of 10^9cm^{-2} .

First, we deposit silver by chronopotentiometry method with a current level of -1.6 mA for 1200 s from the silver bath (TechniSilver, Italgvano). This makes sure that the branching portion of the membrane is clogged. The reduction of silver ions occurs for a potential range of -1.40 and -1.70 V for -1.6 mA of current. Second, we start the deposition of the first gold segment using a current level of -1.6 mA for 3600 s in the gold bath (Orotemp, Italgvano). Our rate of gold deposition is 60 nm/min. The reduction of gold ions occurs for a potential range of -1.70 and -2.65 V for -1.6 mA of current. For subsequent Ag segment that determines the size of our gap,

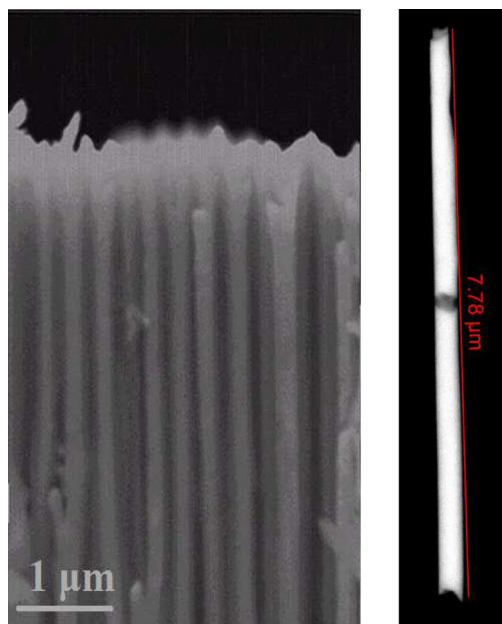


Fig. 3. (a) SEM image of the NWs inside the alumina disc. (b) SEM image of an exemplary NW with a short Ag segment in the middle between long Au segments at the ends (with a total NW length of $7.78 \mu\text{m}$).

we employ a constant current level of -1.6 mA while varying deposition times to make Ag segments of varying lengths tuned from 300 nm down to 20 nm . Our rate of silver deposition is 85 nm/min . Depending on the deposition time, we produced NWs with size controllable nanogaps. Finally, we deposit Au again applying the same current and deposition time with the first Au segment. The total lengths of the Au–Ag–Au NWs were made to be long enough to bridge the electrodes of our microelectrodes (e.g., $7.2 \mu\text{m}$ as in our case) [Fig. 3(a) and (b)].

After finishing the electrodeposition, the backside silver layer is etched with nitric acid solution (HNO_3 of 30% in concentration) and alumina disc is dissolved in sodium hydroxide solution (NaOH of 3 M). The NWs remain in this base solution during dissolution. Subsequently, the NWs are centrifuged three times in water at 3000 r/min for 6 min for rinsing, and three times in methanol at 3000 r/min for 6 min, and finally, dispersed in methanol.

C. Nanogap Formation

After aligning Au–Ag–Au NWs, their middle Ag segments are removed by chemically etching Ag with dilute nitric acid (HNO_3) solution and/or by postbaking process at 200°C for 75 min. This allows for the two ends of the NWs to be automatically and precisely aligned. Difficulties in nanogap formation are sometimes encountered due to the high tendency of Au deposition at the walls of alumina pores, forming a meniscus at the frontline of deposition. This adversely affects the morphology of the NW tips, resulting in a curved profile across the radial axis. In addition to chemical etching, baking process is also employed to remove Ag segments, and this is found to help smoothening of our nanogaps. Such thermal baking enables us to form nar-

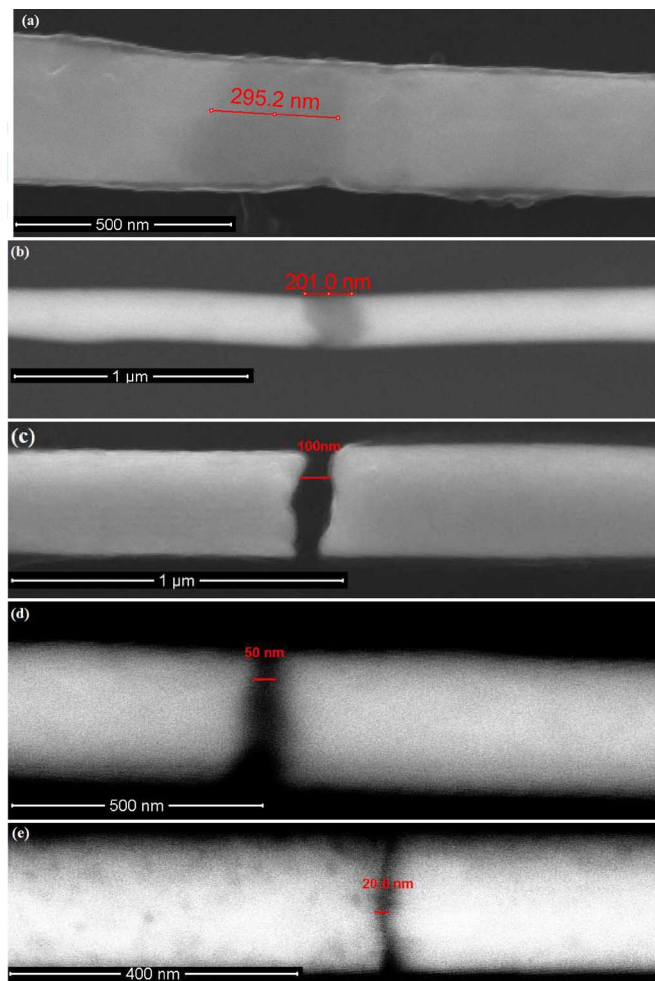


Fig. 4. Example of Au–Ag–Au segmented NWs with tuned Ag segment lengths of (a) 300 nm , (b) 200 nm , (c) 100 nm , (d) 50 nm , and (e) 20 nm .

rower, uniform gaps, and also strengthens the electrical contacts of the gold segment ends of the NWs to the microelectrodes.

We also characterized gap variation and showed that the length of Ag segment can be precisely controlled by the deposition time. The rate of the silver deposition is 85 nm/s . We synthesize Au–Ag–Au segmented NWs that include Ag segments with the lengths of $20\text{--}50\text{--}100\text{--}200\text{--}300 \text{ nm}$ tuned controllably (Fig. 4).

Fig. 5 shows different lengths of Ag segments. The length of these Ag segments is linearly proportional to the total charge injected to the pores of the membrane disc during the electrodeposition process. As we drive constant current (I), the injected charge (Q) is also linearly proportional to the deposition time (t) of silver with the simple relation of $Q = I \times t$, which conveniently allows for controlling the lengths of Ag segments. When Ag segment is selectively chemically etched, we form a nanogap with a size equal to the size of our Ag segment. The deviation among the lengths of Ag segments stems from not being able to drive the same level of current into the pores across the entirety of the alumina disc, and this deviation increases proportionally with deposition time.

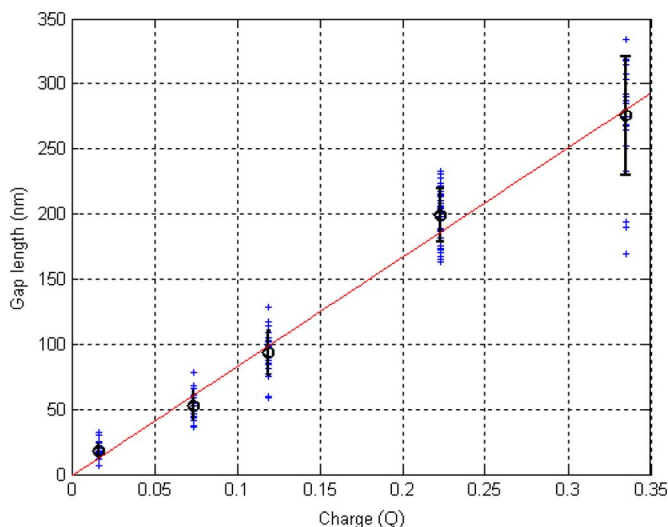


Fig. 5. Gap length versus injected charge in electrodeposition. (Here plus signs represent the lengths of Ag segments; the circles are the average of these lengths and the error bars are their standard deviation.)

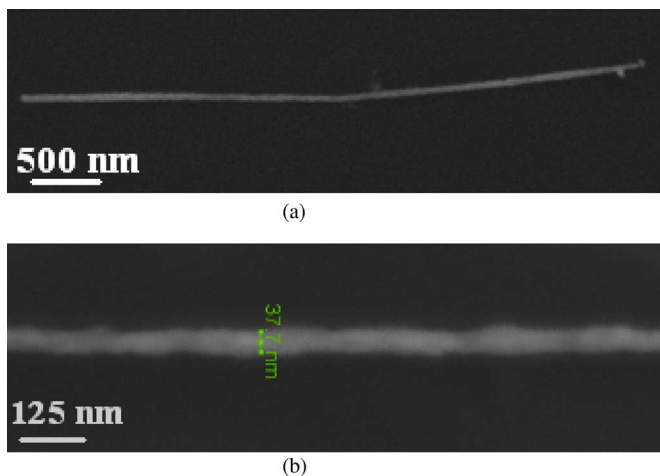


Fig. 6. Nanowire synthesized in track-etched polycarbonate membrane (a) in full view and (b) with a zoom in across its width.

We also synthesized narrow, long NWs, which have diameters of ~ 40 nm with the ultimate goal of capturing and characterizing single NPs. We synthesized Au–Ag–Au NWs using track-etched polycarbonate membranes (Nuclepore) with pore diameters of 15 nm [Fig. 6(a) and (b)]. It is very demanding to fabricate such NW structures even using e-beam lithography (EBL) and focused ion beam (FIB) technology. This in-membrane NW synthesis is, on the other hand, a highly parallel and cheap process, enabling us to synthesize our NWs in massive numbers.

IV. EXPERIMENTS AND RESULTS

A. Dielectrophoretic Alignment of Our Nanowires

Using DEP, we align our NWs on the prefabricated micro-electrode arrays with a purpose of single NW alignment across each of the electrode pairs in the array. After dielectrophoretic

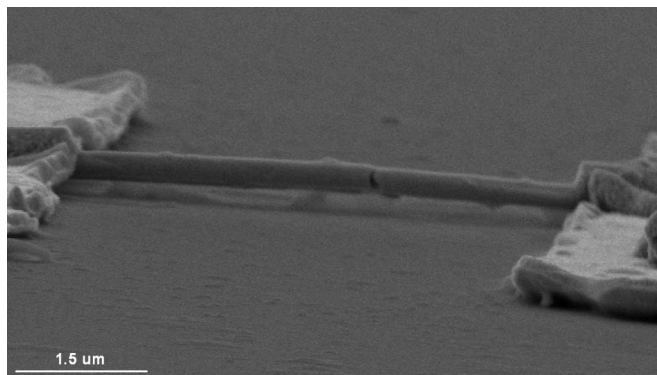


Fig. 7. Suspended nanobridge architecture achieved by DEP of the NW on electrodes.

alignment of these NWs on the electrode arrays, we obtain a 3-D suspended bridge architecture with a width of 300 nm by a length of 7200 nm across a pair of electrodes (Fig. 7). It is challenging to fabricate such a suspended structure by standard nanofabrication techniques such as EBL at these dimensions. EBL provides high resolution (possibly down to 10 nm). It is controllable with high precision. Also, its yield is high in comparison to the DEP alignment process, which is a statistical process. However, EBL is a serial process where a nanostructure is patterned one at a time while DEP is massively parallel.

For our design, we defined some limitation parameters to understand positioning precision. The first one is the translational limit, which is defined by the width of the fingers of our electrodes. It describes the position when the NW is aligned parallel across the fingers. In our case, the fabricated finger width is 3 μm . The other one is the rotational limit, which is defined by the length of the aligned NW and the width of a finger. The maximum angle of the aligned NW with respect to fingers is calculated by the ratio of the length of the aligned NW to the width of the relevant finger. In our case, this is 23° .

In the alignment process, we drop 25–30 μL of the methanol solution containing 7.2- μm -long Au–Ag–Au segmented NWs on our electrode microchip while the signal generator applies an ac bias across the primary pads of our microchip on the corners. Applying the ac signal, the movement of our NWs is monitored in real time through the optical microscope in the probe station. The drop-casting and alignment steps are repeated two to three times to allow the NWs to flow in the medium without getting stuck on the chip base, and thus get manipulated by dielectrophoretic force across the electrode fingers. For our devices on Si/Si_xO_y substrate, we achieve alignments by applying a sinusoidal wave of $V_{pp} = 50$ V across our primary pads for 30–45 s at 10 kHz by observing the chip under the microscope. In the alignment process, our NWs polarize under the applied ac field. As they are more polarizable than the dielectric medium, they align moving toward the electrode fingers as a result of the positive dielectrophoretic force, which is maximized at the high field gradients across the fingers.

The required ac frequency applied for manipulating NWs depends on the dielectric constants and conductivity levels of

the medium and NWs. The resulting dielectrophoretic force is strong for Au–Ag–Au NWs in methanol at 10 kHz. Below 500 Hz, weaker manipulation of NWs is observed. For higher frequencies, higher field is needed to achieve the same level of dielectrophoretic force, as the induced charges cannot follow the change of polarization with the applied electrical field. For water soluble particles, we apply DEP in water at a different frequency of 1 MHz, since the dielectric property and the conductivity of the medium substantially changes compared to methanol.

A certain level of potential difference needs to be applied across the main pads to achieve required dielectrophoretic force on NWs. DEP force is directly proportional to the gradient of the square of the electric field (∇E_{rms}^2). Therefore, in principle, the resulting DEP force is moving the NWs toward higher field gradient. Thus, ideally, the NWs should be directed to the highest field gradient where the fingers of the electrodes are located. In reality, however, there are other forces that affect the movements of NWs. Brownian motion and thermal effects act against dielectrophoretic manipulation. Moreover, if a NW comes close to or touches the surface, some other short-range strong forces may start to be effective, too. Thus, the dielectrophoretic force must be strong enough to overcome all these other forces for successful manipulation of NWs on to the fingers of the electrodes. For $V_{\text{pp}} < 50$ V, we observed that the resulting dielectrophoretic force is not strong enough to form bridges between microelectrodes by aligning NWs. For $V_{\text{pp}} > 50$ V, undesired multiple NW alignments are observed to be forming on our microelectrode chips. Therefore, we find the optimal $V_{\text{pp}} = 50$ V in our case.

Another important parameter of the alignment process is the NW concentration. Increasing NW concentration also increases the number of single alignments, but this comes at the cost of increased multiple alignments at the same time. Therefore, we use a reasonable NW concentration of 2.5×10^7 mL⁻¹, which reproducibly achieves high yield alignments more than 20%.

The viscosity of the host medium is also an effective parameter for alignment. Less viscous medium is more useful to manipulate suspending NWs. As we cannot specifically analyze the effect of viscosity in liquids solely as they also have different dielectric constants, we observe the effects of these two properties together. We observe that methanol, which has a viscosity of 0.584 cP at 25 °C and a dielectric constant of 32.6, increases the yield of the alignment compared to isopropanol, which has a viscosity of 1.96 cP at 25 °C and a dielectric constant of 20.18.

Following the NW alignment, after the field is applied and before the methanol dries over the chip, we clean the chip gently by nitrogen gun to get rid of the unaligned NWs. Additionally, if desired, 20-nm Ti/300-nm-thick Au clamps are patterned on the two sides of the microelectrode arrays to anchor the NWs on the fingers of electrodes (Fig. 8). Also, these clamps facilitate a better contact between the electrodes and NWs. At this stage, we obtain I – V characterization of the on-chip-integrated NWs (by applying a compliance of 100 nA to prevent the NWs potentially from heating and melting with large current densities).

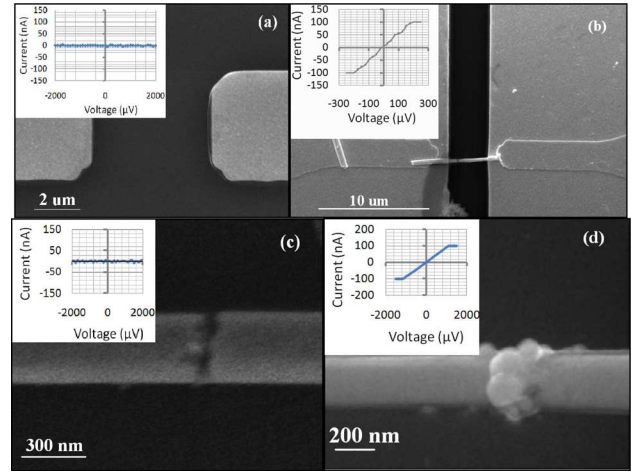


Fig. 8. SEM images of (a) microelectrode fingers before NW alignment (along with their I – V showing *open circuit* in the inset), (b) aligned Au–Ag–Au segmented NW by using DEP and clamps (along with its I – V showing *electrical conduction* in the inset), (c) NW after gap formation in the middle (along with its I – V showing *open circuit* again in the inset), and (d) NPs captured in the size controllable nanogap by using DEP (along with I – V showing *electrical conduction* in the inset).

B. Dielectrophoretic Capturing of Nanoparticles

We achieve high electric fields across the etched nanogap between Au segments of the NWs. As a proof-of-concept demonstration, gold NPs that have a diameter distribution of 80–120 nm in aqueous solution are dropped on the chip where they are initially randomly distributed. Applying a sinusoidal wave with V_{pp} of 10 V at 1 MHz across primary pads for 45 s, we apply electric field between nanogaps formed in the middle of the NWs. Polarized Au NPs are then trapped in these nanogaps where the field gradient and the resulting dielectrophoretic force are large. Finally, after capturing NPs in the gap, we observe strong electrical conduction again across the electrodes.

This approach combines the benefits of using segmented NWs and their alignment using DEP. One of the key benefits in this combined approach is that the aligned NWs automatically provide electrical contacts to the captured NPs to allow for their probing and characterization at the nanoscale. Also, since nanogaps are formed with the selective removal of the midsection of the NWs, the two ends of the NWs are automatically self-aligned. In the process, before the removal of middle Ag section, we observe electrical conductance through the continuous Au–Ag–Au NW by probing the two connected microelectrodes [shown in the inset of Fig. 8(b)]. However, after the removal of middle section, we observe the open circuit behavior, confirming the formation of a full gap [shown in the inset of Fig. 8(c)].

Finally, after capturing NPs in the gap, we observe strong electrical conduction again across the electrodes. This indicates that metal NPs are captured in the nanogap to bridge and retain the conductivity. This implies that our NW arrays with size controllable nanogaps potentially provide a nanoscale device platform convenient for NP capturing and electrical characterization [Fig. 8(d)].

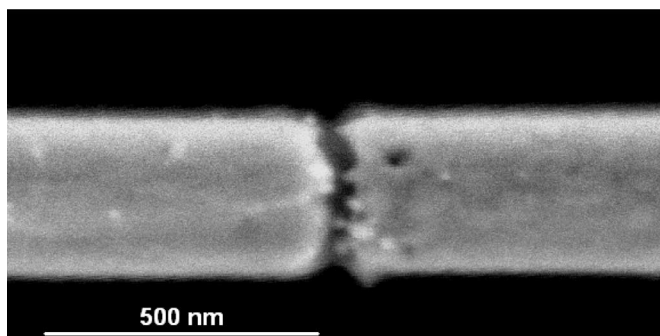


Fig. 9. Nanocrystals (20 nm) captured by DEP in 50-nm gap.

Furthermore, with the help of UV illumination at an optical wavelength of 254 nm, we achieve the successful capturing of cadmium selenide/zinc sulfide (CdSe/ZnS) nanocrystals (NCs) in toluene with diameters of ~ 20 nm (Fig. 9). For this, we apply a sinusoidal wave with a magnitude of $V_{pp} = 10$ V between the primary pads of our chip. Without using UV illumination, we observe no capturing of NCs under the identical experimental conditions. In the previous literature, NCs were captured in 200-nm-wide gaps fabricated by EBL using very high dc biases [13]. In our case, using reasonable ac field, the UV illumination increases the polarizability of the NCs, by photogenerating electron-hole pairs inside them. Increased polarizability directly affects the net DEP force acting on these NCs. This might open up possibilities for the construction of semiconductor NC/nanorod embedded nanoscale devices (e.g. [14]).

V. CONCLUSION

In our study, we proposed and demonstrated constructing an on-chip NW device platform for manipulating, capturing, and electrical characterization of NPs. For this purpose, we synthesized Au-Ag-Au segmented NWs and dielectrophoretically aligned them on our microfabricated array of electrodes. After the NWs were aligned, we selectively removed the middle Ag segments, obtaining self-aligned pairs of end Au segments with a nanogap forming between them. Also making a systematic study of the gap size variation, we opened nanogaps of different sizes, which can be used for different characterizations and different applications, showing the feasibility of tuning gap formation controllably.

In each step of integration, we made I - V measurements and SEM imaging to show our proof-of-concept demonstration of the NW platform integrated with microelectrodes. In this characterization sequence, we first observed an open circuit between the empty microelectrode fingers before NW alignment. Second, a short circuit was obtained when we aligned the Au-Ag-Au segmented NWs on the microelectrodes. Third, an open circuit was achieved between the Au ends of the NWs when we formed nanogaps by removing the middle Ag segments. Finally, a short circuit was accomplished between the Au ends of the NWs after capturing of gold NPs in the nanogaps. This shows that we electrically contacted the NPs trapped in these nanogaps to the NWs and the NWs to the microelectrodes. We

also dielectrophoretically captured NCs. We show that these semiconductor NPs can be captured, enabling their use possibly in optoelectronic nanodevice architectures. The fabrication of such devices is currently underway. Our approach holds great promise for full integration from a single NP to NWs to microelectrodes to macroprobes on a single chip, possibly enabling such single NP devices.

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Ramazan Kizil, photograph and biography not available at the time of publication.