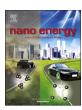
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# Full paper

# Nanocrystal light-emitting diodes based on type II nanoplatelets

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### ABSTRACT

Colloidal semiconductor nanoplatelets (NPLs) have recently emerged as a new family of semiconductor nanocrystals with distinctive structural and electronic properties originating from their atomically flat architecture. To date, type II NPLs have been demonstrated to possess great potential to optoelectronic applications, such as solar cells and lasers. Herein, nanocrystal light-emitting diodes (LEDs) based on type II NPLs have been developed. The photoluminescence quantum yield of these used type II NPL (CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown) is close to 85%. By exploring an effective inverted structure with the dual hole transport layer, the NPL-LEDs exhibit i) a turn-on voltage of 1.9 V, ii) a maximum luminance of 34520 cd m $^{-2}$ , iii) an EQE of 3.57% and a PE of 9.44 lm W $^{-1}$ . Compared with previous NPL-based LEDs, the performance of our devices is remarkably enhanced. For example, the luminance is 350-fold higher than the best inverted NPL-based LED. The findings may not only represent a significant step for NPL-based LEDs, but also unlock a new opportunity that this class of type II NPLs materials are promising for developing high-performance LEDs.

### 1. . Introduction

Colloidal semiconductor nanocrystal light-emitting diodes (LEDs) have been actively studied for utilization in displays and lighting because they exhibit many exceptional properties including high efficiency, good color purity, low power consumption, light weight, long lifetime and flexibility [1–3]. Since the first demonstration of colloidal quantum-dot LED (QD-LED) [4], a large number of efforts have been made to enhance the device performance via the optimization of both material syntheses and device architectures. [5–7] As an impressive consequence, the external quantum efficiency (EQE) of QD-LEDs has reached 20.5% by inserting an insulating layer improving the device structure, which is comparable to that of state-of-the-art organic LEDs (OLEDs) [8].

Semiconductor nanoplatelets (NPLs), also known as colloidal quantum wells, have been introduced as a new class of solution-processed two-dimensional (2D) nanocrystals in recent years [9–12]. Aside from the core-only NPLs having different vertical thicknesses and tuned chemical compositions, various NPLs with heteroarchitectures (e.g., core/shell, core/crown, core/crown/shell) have also been synthesized

to engineer the electronic structure and optical properties [13-15]. Thanks to the tight quantum confinement only in the vertical direction, NPLs possess many unique thickness-dependent optical characteristics, such as narrow emission band, ultrashort radiative fluorescence lifetime, giant oscillator strength transition and extremely large linear/ nonlinear absorption cross-sections [16-18]. Therefore, NPLs are highly attractive for optoelectronic applications including solar cells, lasers and LEDs [19-21]. However, compared to other types of semiconductor nanocrystal LEDs (e.g., those exploiting QDs, nanorods, and dot-inrods), the performance of NPL-based LEDs lags far behind. For example, Chen et al. designed a red NPL-based LED by using the core/shell CdSe/ CdZnS NPLs, achieving a maximum luminance of 4499 cd m<sup>-2</sup>, an EQE of 0.63% and a turn-on voltage of 4.7 V, which is the best red NPLbased LED thus far. [22] Fan et al. reported a green NPL-based LED by utilizing the CdSe<sub>1.v</sub>S<sub>v</sub> NPLs, obtaining a maximum luminance of ~ 90 cd m<sup>-2</sup>, which is the best NPL-based LED using inverted structure [23]. More recently, Ling et al. have achieved a green NPL-based LED by employing organometal halide perovskite CH3NH3PbBr3 NPLs, yielding a maximum power efficiency (PE) of 1.0 lm W<sup>-1</sup>, the best PE for NPL-based LED [24]. Therefore, the question is: is it possible to

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further enhance the performance of NPL-based LEDs?

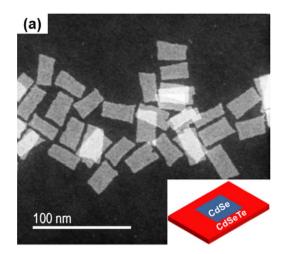
Depending upon the relative alignment of the conduction band (CB) and valence band (VB) positions of the core material and those of the crown or shell materials, these hetero-NPLs can behave as type I or type II heterostructures [25–27]. For type I NPLs, both holes and electrons wave functions are confined in the same region due to a wider band gap crown or shell barrier. For type II NPLs, the electrons and holes wave functions are staggered with respect to each other. Over the last few years, type I and core-only NPLs have been synthesized and widely investigated, while negligible attention has been paid to type II NPLs. In fact, the first type II NPLs were just synthesized only three years ago [28]. To date, type-II NPLs have been demonstrated to possess great potential in optoelectronic applications such as solar cells and lasers [29]. In the terms of LED applications, previous NPL-based LEDs have been constructed by using type I NPLs and core-only NPLs. Unlike type I and core-only structures, type II NPLs can possess higher emission quantum yield and reduced overlap between the absorption and emission spectrum [30], rendering that type II NPLs are potentially very promising and beneficial to LEDs. However, no type II NPL has been reported to develop LEDs, to the best of our knowledge.

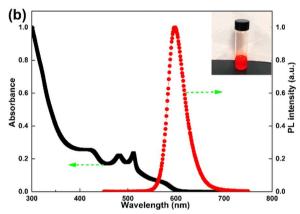
In this paper, type II NPLs, for the first time, have been used to develop LEDs. The photoluminescence quantum yield (PLQY) of the employed type II NPL (CdSe/CdSe $_{0.8}$ Te $_{0.2}$  core/crown) is close to 85%, which is the highest for NPLs utilized among all NPL-based LEDs. By taking advantage of an effective inverted structure with the dual hole transport layer (HTL), the LED can exhibit a very low turn-on voltage of 1.9 V, a maximum luminance of 34520 cd m $^{-2}$ , an EQE of 3.57% and a PE as high as 9.44 lm W $^{-1}$ . Compared with previous NPL-based LEDs, the performance of our device is remarkably enhanced. For example, the luminance is 350 times higher than the previous best inverted NPL-based LED.

# 2. Results and discussion

# 2.1. Sample characterization

Colloidal CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs were synthesized following the reported procedures with slight modifications. [30] Further details of the syntheses can be found in the Supporting information. CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs exhibit roughly a rectangular shape, as can be seen from the high-angle annular dark-field scanning transmission electron microscopy (HAADF-STEM) image shown in Fig. 1a. In average, these type II core/crown NPLs possess a rather uniform lateral size of  $\sim\,25\,\text{nm}$  in length by  $\sim\,8\,\text{nm}$  in width. The optical properties of these type II CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs were characterized via ultraviolet-visible absorbance and PL spectroscopy. Fig. 1b depicts the absorption and steady-state PL characteristics of the NPLs in toluene at room temperature. Unlike the core-only and type I NPLs previously employed in LEDs showing a large overlap and an extremely small Stokes shift between the absorption and PL spectra [22-24], type II NPLs exhibit considerably small spectral overlap between the absorption and PL. This decreased overlap enhances the QY of the film by decreasing the reabsorption rate in this system. Therefore, type II NPLs are promising for LEDs with their superior QY in film. The sharp excitonic features at  $\sim 511$  and  $\sim 482\,\mathrm{nm}$  observed in the absorption spectrum of CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs are associated with the electron-heavy hole and electron-light hole transitions, respectively, in the CdSe cores. The broad absorption feature beyond  $\sim$ 511 nm is associated with the  $CdSe_{0.8}Te_{0.2}$  crown and this broadening can be explained by the fluctuation of the Te composition in the CdSe<sub>0.8</sub>Te<sub>0.2</sub> crown [30]. The normalized PL spectrum of the type II CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs shows a red emission peak at ~ 599 nm. To demonstrate that our NPLs structure is really type-II, we performed energy-dispersive X-ray spectroscopy (EDX) measurements on a single NPL using STEM at the core and crown regions. The results of this measurements are given in Fig. S1 (Supporting information).





**Fig. 1.** (a) A HAADF-STEM image of the type II  $CdSe/CdSe_{0.8}Te_{0.2}$  core/crown NPLs. Inset: a schematic structure of the NPLs. (b) Room-temperature absorbance and PL of NPLs in the toluene solution. Inset: a photograph of the NPLs solution.

These results show that there is only Se at the core while both Se and Te exist at the crown region, signifying the successful growth of CdSeTe crown layer on CdSe core. This is also in agreement with our optical measurements and previous reports. In addition, the excitonic features observed in the absorption spectrum and PL emission characteristics given in Fig. 1b are in agreement with previously published results, [30] strongly indicating that these NPLs are type-II CdSe/CdSeTe NPLs.

# 2.2. Device design strategy

To achieve high-performance LEDs based on type II NPLs, here several strategies were adopted. First, an inverted structure was employed, which is advantageous for display applications, since the bottom transparent cathode of inverted LEDs can be directly connected to the low-cost n-type metal-oxide or amorphous-silicon thin film transistors [31]. In fact, high-performance inverted QD-LEDs have been realized in recent years. For example, inverted QD-LEDs can show a maximum EQE of 18%, close to the theoretical maximum of 20% [32]. However, the best inverted NPL-based LED exhibits only a maximum luminance of  $\sim 90\,\mathrm{cd\,m^{-2}}$ . [23] To loosen the bottleneck, an urgent effort needs to be made to enhance the performance of inverted NPL-based LEDs.

Besides, a hybrid structure was exploited, where the NPL emitting layer (EML) is sandwiched between the inorganic zinc oxide (ZnO) electron transport layer (ETL) and organic HTL. Such a hybrid structure has the potential to achieve the high performance since i) various organic small molecules with high hole mobility can be selected as the effective HTLs and ii) HTLs can be deposited by thermal evaporation technique without any damage to the underlying layer, avoiding the

solvent penetrating problem [32].

Then, unlike previously developed NPL-based LEDs that utilized the exchange of long chain ligands with the shorter ones, [22] a recyclable treatment on the type II CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs was applied using hexane/ethyl acetate mixed solvent to control the surface ligand density here. Surface ligands are important tools for achieving successful surface passivation, elimination of surface defects, providing ink stability, and high PLQY. However, excessive ligands can form insulating layers since oleylamine and oleic acid organics utilized in the synthesis of the type II CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs possess low electric conductivity, which in turn reduces the charge injection and transport inside the devices [33]. To accomplish the trade-off, the control of ligand density on NPL surfaces is used because it is more suitable than the surface ligand exchange to promote the performance of LEDs [34]. After the recyclable treatment, the PLQY of the new type II CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> core/crown NPLs is close to 85%, which is much higher than that of previous NPL-based LEDs (i.e., 20% for the best red NPL-based LEDs) [22]. The PLQY of 85% is also higher than that of previous works [35,36]. Besides, although type II NPLs have been reported, negligible attention has been paid on their surface ligand density. Since solvents play a vital role in controlling ligand density, [34] the presented recyclable treatment can be an effective approach to manage the ligand density of type II NPLs, which is beneficial to deeply understand type II NPLs and provides an exploration for the LED applications.

Finally, to boost the performance, the charge balance should be enhanced. In LEDs, the performance can be analyzed by EQE and PE. The EQE is defined as follows: [37]

$$EQE = \eta_{out} \cdot r \cdot q \cdot \gamma \tag{1}$$

where  $\eta_{out}$  is the outcoupling factor, r is the fraction of excitons that can potentially radiatively decay, q is the PLQY of emitters, and  $\gamma$  is the charge balance ( $\gamma \leq 1$ ). The internal operation of the device usually does not influence  $\eta_{out}$  and  $\eta_{out}$  is about 0.2 according to the classical ray model. [35] Additionally, the terms r and q will be mostly set with the emitters. Hence, from the perspective of device engineering, the EQE is mostly sensitive to  $\gamma$ . Assuming the emission pattern is the Lambertian type, the connection between EOE and PE can be expressed as: [38]

$$PE \propto \frac{EQE}{U} \tag{2}$$

where U is the driving voltage. Simply, PE can be considered as:

$$PE \propto \frac{\gamma}{U} \tag{3}$$

Therefore, to achieve high PE,  $\gamma$  should be high while the U should be low. In other words, charges should be not only well balanced but also effectively transported. However, because of the unfavorable energy barriers between different layers, together with the fact that holes are minor charges in inorganic materials, charges balance and transport are usually unsatisfactory [8]. To address these issues in NPL-based LEDs, the exploration of ideal HTL is one of the most effective ways, yet virtually no work has been reported in this direction. Here, we first used 4,4'-bis-(m-tolyphenylamino)biphenyl (TPD), 4,4',4"-Tri(N-carbazolyl) triphenylamine (TCTA) and 4,4-N,N-dicarbazolebiphenyl (CBP) as the single HTL to investigate the performance of NPL-based LEDs. Furthermore, we explored a dual-HTL to enhance the performance.

Taking the above factors into account, high-performance LEDs based on type II NPLs may be expected. As shown in Fig. 2, the structure of LEDs with single HTL is indium tin oxide (ITO)/ ZnO/ type II NPL/ HTLs (60 nm)/ MoO<sub>3</sub> (10 nm)/ Al (100 nm), where ITO is the cathode, ZnO is the ETL, NPL is CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> (6 mg/mL, 2000 rpm, 45 s), MoO<sub>3</sub> is the hole injection layer (HIL), Al is the anode, and HTLs are TPD (device R1), TCTA (device R2) and CBP (device R3).

#### 2.3. Device with single HTL

As shown in Fig. 3a and Table 1, Device R3 shows the lowest voltage and the highest luminance among all LEDs with a single HTL, suggesting that CBP is the best single HTL. For example, the turn-on voltage (the voltage when the luminance can be detected,  $\geq 0.01$  cd m $^{-2}$ ) of Device R3 is 2.2 V, which is lower than those of Devices R1 (2.6 V) and R2 (3.0 V). At 1000 cd m $^{-2}$ , the voltage of Device R3 is 4.3 V, which is also lower than those of Devices R1 (4.6 V) and R2 (5.1 V). On the other hand, the maximum luminance of Device R3 is 22920 cd m $^{-2}$ , which is significantly higher than those of Devices R1 (8228 cd m $^{-2}$ ) and R2 (14350 cd m $^{-2}$ ).

The phenomenon that facilitates Device R3 to exhibit the best performance among LEDs with a single HTL can be explained as follows. Since all layers in these LEDs are similar except the HTL, the low voltages and high luminances can be attributed to the selection of CBP. For HTLs, hole mobility, hole injection efficiency, hole-blocking ability, and energy gap play critical roles in LEDs. [43,44] For Device R1, although TPD has the highest hole mobility among these single HTLs  $(1.4 \times 10^{-3} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1})$  and strong electron-blocking ability due to its LUMO of 2.4 eV, the HOMO of TPD is not deep enough (5.4 eV) [41]. As a result, there is a large energy barrier between the NPL and TPD  $(\sim 0.6 \text{ eV})$  (Fig. 2c), which is unfavorable for the hole transport, leading to a large number of holes accumulated at the EML/ TPD interface. Such hole accumulation can easily result in NPL charging, which is detrimental to the device performance since such charging can lead to the NPL luminescence quenching via the nonradiative Auger recombination mechanism. [45] Hence, although the very high hole mobility and narrow energy gap of TPD render that device R1 exhibits a high current density, [46] Device R1 shows unsatisfactory luminance and poor efficiency due to the Auger recombination (Fig. 3b). For Device R2, because the hole mobility of TCTA is relatively low  $(8.0 \times 10^{-5} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1})$ , the hole transport ability is poor for the TCTA material, giving rise to the high voltage, although TCTA has a stronger electron-blocking ability than CBP because the lowest unoccupied molecular orbital (LUMO) of TCTA (2.3 eV) is higher than that of CBP (2.5 eV) [41,42]. For Device R3, since CBP possesses a high hole mobility  $(10^{-3} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1})$ , holes can be effectively transported. [47] In addition, the highest occupied molecular orbital (HOMO) of CBP is 5.9 eV, which is almost the same as that of the NPL, indicating that holes can be easily transported from CBP to the NPL [42]. Therefore, CBP is the most suitable single HTL to enhance the charge balance and transport, allowing for the low voltage.

According to Eqs. (1) and (3), because of the well balanced and effectively transported charges in Device R3, both the EQE and PE can be enhanced. As shown in Fig. 3b and Table 1, the efficiency of Device R3 is higher than those of Devices R1 and R2. For instance, the maximum EQE of Device R3 is 2.01%, higher than those of R1 (0.49%) and R2 (1.07%). At  $1000 \text{ cd m}^{-2}$ , the EQE of Device R3 is 1.41%, higher than Devices R1 (0.43%) and R2 (1.02%). Also, the maximum PE of R3 is  $4.64 \text{ lm W}^{-1}$ , again higher than R1 (0.75 lm W $^{-1}$ ) and R2 (1.49 lm W $^{-1}$ ). At  $1000 \text{ cd m}^{-2}$ , the PE of Device R3 is  $2.4 \text{ lm W}^{-1}$ , similarly higher than R1 (0.66 lm W $^{-1}$ ) and R2 (1.48 lm W $^{-1}$ ).

### 2.4. Device with dual-HTLs

In OLEDs, multilayer structures have been usually employed (i.e.,  $\geq 3$  electron injection layers, p-doping/n-doping layers), which are used to effectively enhance the charge injection and balance. [48–50] However, multilayer structures are difficult to be introduced in colloidal semiconductor nanocrystal LEDs due to the solution-processed technique, limiting the device performance [37]. Hence, although the structures of previous NPL-based LEDs are simple (i.e., using a single HTL and a single ETL), the charge injection and balance may be not optimized. Galvanized by the fact that the performance of NPL-based LEDs can be greatly affected by different HTLs, we further improved the

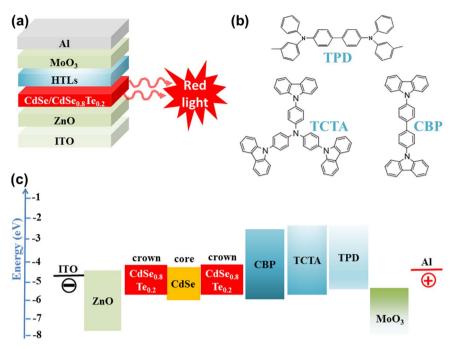


Fig. 2. (a) The schematic structure of LEDs based on type II NPLs. (b) The chemical structure of HTLs. (c) The proposed energy levels of the LEDs based on type II NPLs. The values are taken from the literature. [8,22,39–42].

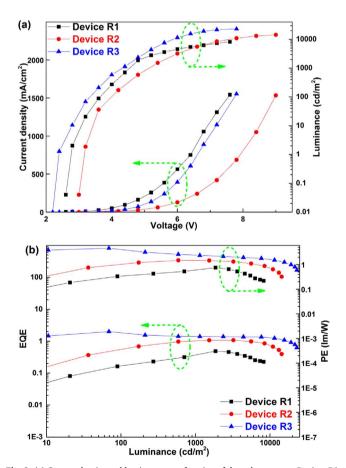


Fig. 3. (a) Current density and luminance as a function of the voltage across Devices R1, R2 and R3. (b) EQE and PE as a function of the luminance for Devices R1, R2 and R3.

device structure by adopting a dual-HTL architecture. Since the HOMO of TCTA is appropriately located between those of NPLs and TPD, we developed a LED with the dual-HTL TCTA (10 nm)/TPD (50 nm) (Devices R4), while other layers are similar to those of the LED with a

Table 1
Summary of the performance levels of LEDs.

Device	V <sub>on/1000</sub> <sup>a</sup> (V)	EQE <sub>max/1000</sub> <sup>b</sup> (%)	PE <sub>max/1000</sub> <sup>c</sup> (lm W <sup>-1</sup> )	L <sub>max</sub> <sup>d</sup> (cd m <sup>-2</sup> )	CIE <sup>e</sup>
R1	2.6/4.6	0.49/0.43	0.75/0.66	8228	(0.61, 0.38)
R2 R3	3.0/5.1 2.2/4.3	1.07/1.02 2.01/1.41	1.49/1.48 4.64/2.40	14350 22920	(0.61, 0.38) (0.61, 0.38)
R4	1.9/3.4	3.57/3.20	9.44/7.10	34520	(0.61, 0.38)

- $^{\rm a}$  Turn-on voltage and the voltage at 1000 cd m  $^{\rm -2}$ .
- <sup>b</sup> Maximum EQE and EQE at 1000 cd m<sup>-2</sup>.
- $^{\rm c}$  Maximum PE and PE at 1000 cd m  $^{-2}$ .
- d Maximum luminance.
- $^{\rm e}$  The Commission International de l'Eclairage (CIE) coordinates at 1000 cd m  $^{-2}.$

single HTL. The cross-sectional scanning electron microscope (SEM) image of Device R4 is depicted in Fig. S2 (Supporting information).

As shown in Fig. 4a and Table 1, Device R4 shows low voltage and high luminance. For example, the turn-on voltage of Device R4 is 1.9 V, which is the lowest among NPL-based LEDs. The sub-bandgap voltage is also observed by other groups, which can be attributed to the efficient charge injection at low bias [8,32]. At 1000 cd m<sup>-2</sup>, the voltage is still as low as 3.4 V. Remarkably, the maximum luminance of Device R4 is 34520 cd m<sup>-2</sup>, which is the highest for NPL-based LEDs. Due to the effective dual-HTL, the efficiency of device R4 is also significantly enhanced. For example, Device R4 can exhibit a maximum EQE of 3.57% and a maximum PE of 9.44 lm W<sup>-1</sup>, which is the highest for NPL-based LEDs. To the best of our knowledge, this device outperforms previous NPL-based LEDs [22–24]. For example, the luminance of Device R4 with the inverted structure is 350-fold larger than that of the best inverted NPL-based LED [23].

Fig. 5 depicts the electroluminance (EL) properties of Device R4. Unlike previous NPL-based LEDs of which the EL spectra are obviously redshifted compared with their PL spectra here, the EL spectrum (599 nm) almost coincides with the PL spectrum (599 nm) for Device R4 (Fig. 5a). Such well complete overlap between the EL and PL spectrum demonstrates the efficient recombination of holes and electrons on NPLs in the LEDs. [51] Besides, the EL spectra are very stable at different luminance levels, as shown in Fig. 5b. In addition, the color

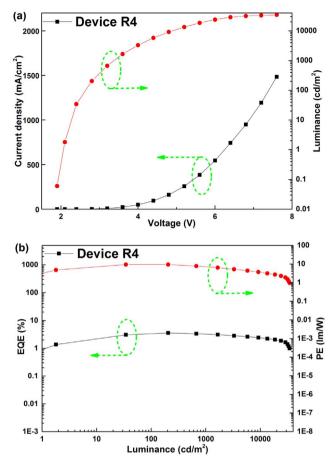


Fig. 4. (a) Current density and luminance of device R4. (b) EQE and PE of device R4.

of Device R4 is well located in the red region with the CIE coordinates of (0.61, 0.38), as shown in Fig. 5c and d.

# 2.5. Insights of the effect of the dual-HTL

Upon using the dual-HTL TCTA/TPD, the performance of Device R4 was made significantly higher than that of the device with the best single HTL (Device R3). Hence, to understand the origin of the high performance of Device R4, the effect of the dual-HTL TCTA/TPD needs to be unveiled. Particularly, the roles of the dual-HTL and the best single HTL CBP in the device performance will need to be comprehensively discussed.

First, it is noted the electron mobility of CBP is as high as  $10^{-4}\,\mathrm{cm}^2$  V<sup>-1</sup> s<sup>-1</sup>, [47] indicating that electrons can escape from the EML when they obtain enough energy at high voltages/ luminances, although there is a LUMO barrier between the CBP and the NPL. Since the electron leakage is detrimental to the efficiency, [43] CBP is not so ideal to be the HTL. On the other hand, TCTA is a p-type material and its electron mobility can be negligible, [41] implying that electrons can be well confined in the EML. Additionally, the LUMO of TCTA is higher than that of CBP, indicating that TCTA possesses a better electron-blocking ability, which can further reduce the electron leakage. As a result, more electrons can meet more holes to form more excitons in Device R4 than Device R3, leading to higher performance.

Besides, the hole injection efficiency of HTLs is essential for high performance. [43] To determine the hole injection efficiency of CBP and TCTA/TPD, hole-only devices have been fabricated. [46] The structures are ITO/ poly(3,4-ethylenedioxythiophene): polystyrene sulfonate (PEDOT: PSS, 40 nm)/ CdSe/CdSe<sub>0.8</sub>Te<sub>0.2</sub> (6 mg/mL, 2000 rpm, 45 s)/ CBP or TCTA/TPD (60 nm)/ MoO<sub>3</sub> (5 nm)/ Al (100 nm), where PEDOT: PSS is a well-known p-type material and

functions as the electron blocking layer for the hole-only devices [6]. As shown in Fig. 6, the TCTA/TPD based device shows higher current density than the CBP based device, indicating that the dual-HTL TCTA/ TPD possesses a higher hole injection efficiency than the single HTL CBP. Hence, holes are more easily injected from the TCTA/TPD dual-HTL to the EML. The reason why TCTA/TPD possesses a higher hole injection efficiency can be explained as follows. On one hand, when inserting a thin MoO<sub>3</sub> between ITO and TPD, an Ohmic hole injection can be easily formed, [52] implying that holes are barrier-free transported to the dual-HTL. However, despite the fact that MoO<sub>3</sub> can enhance the hole injection of ITO/ CBP interface, it is not as efficient as that of the ITO/TPD interface [53]. On the other hand, since the HOMO of TCTA (5.7 eV) is well located between TPD (5.4 eV) and NPLs (~ 6.0 eV), a stepwise structure is formed, which is favourable to the hole transport [43]. Although the hole mobility of TCTA is not high, the reduced energy barrier resulted from the dual-HTL can vastly enhance the hole transport since the hole mobility has less influence on the performance compared with the energy level alignment [54-56]. Hence, holes can be easily injected into the EML via the stepwise structure. In other words, more holes can be injected into the EML in Device R4 than Device R3, which is key to the high performance.

Considering the high electron mobility of ZnO  $(1.3 \times 10^{-3} \, \mathrm{cm^2 \, V^{-1}} \, \mathrm{s^{-1}})$  and small LUMO barrier between ZnO and NPLs, [57] electrons are very easily injected into the EML from the cathode. Hence, the number of electrons can surpass that of holes, leading to a poor charge balance. In fact, when using ZnO as the ETL, electrons are usually the major carriers in colloidal semiconductor nanocrystal LEDs, regardless of inverted or normal structures [37]. Previously, due to the strong electron ability of ZnO, Dai et al. used an insulating layer 6 nm poly(methylmethacrylate) to limit the electron transporting from ZnO into the EML, which is expected to balance the number of electrons and holes. [8] Here, to achieve the charge balance, we adopt an alternative way by increasing the hole injection efficiency from the dual-HTL TCTA/TPD instead of decreasing the electron injection efficiency. Therefore, due to the better charge balance, it is reasonable that Device R4 outperforms Device R3 according to Eqs. (1) and (2).

Furthermore, charging can degrade the emitting properties of NPLs, regardless of the electroluminescence or photoluminescence [8]. Due to the different workfunction, a spontaneous charge transfer process can occur upon NPLs directly contacting the charge transport layer, resulting in charged NPLs, which causes inefficient trion emissions [58,59]. As shown in Fig. 7, the average photoluminescence lifetime of the NPL film is 6.5 ns, which is decreased to 0.72 ns after depositing the top CBP film while remained 2.39 ns after depositing the TCTA/TPD film. Therefore, the dual-HTL TCTA/TPD is beneficial to maintain charge neutrality of NPL emitters and preserve their superior emitting properties, which is another factor that contributes to the high performance of Device R4.

# 3. Conclusions

In summary, LEDs based on type II NPLs have been successfully developed. By incorporating the highly efficient CdSe/CdSe $_{0.8}$ Te $_{0.2}$  core/crown type II NPLs in effective inverted architecture with a dual HTL, low voltage, high luminance and high efficiency has been simultaneously achieved. This NPL-LED exhibits a turn-on voltage of 1.9 V, a maximum luminance of 34520 cd m $^{-2}$ , a high EQE of 3.57% and a high PE of 9.44 lm W $^{-1}$ , which are remarkably superior to the reports of previous NPL-based LEDs (i.e., 2 orders of magnitude higher than the previous best inverted NPL-based LED). The findings may not only represent a significant step for the NPL-based LEDs, but unlock a new opportunity that this class of materials (type II NPLs) are promising for developing high-performance LEDs.

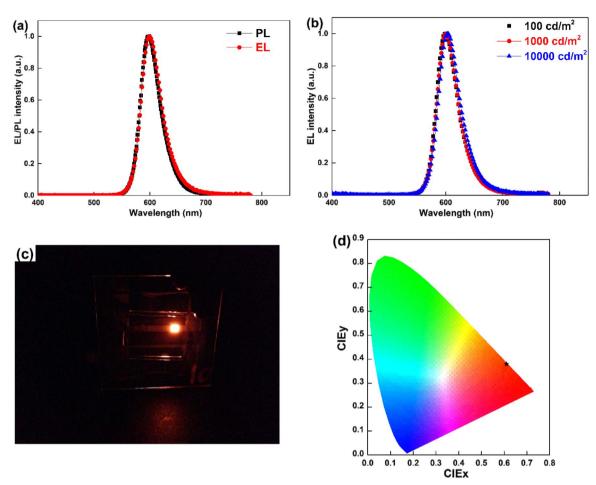


Fig. 5. (a) PL and EL spectra at  $1000 \, \text{cd m}^{-2}$  of Device R4. (b) Spectra at various luminance levels of R4. (c) The photograph of R4 at  $1000 \, \text{cd m}^{-2}$ . (d) The CIE coordinates of (0.61, 0.38) at  $1000 \, \text{cd m}^{-2}$ .

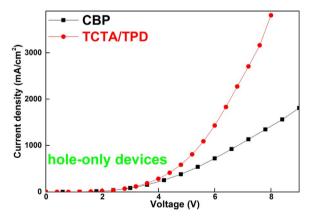


Fig. 6. Current density vs bias voltage in hole-only devices.

# 4. Methods

Without breaking the vacuum, all material layers were thermally deposited at a base pressure of  $2.0\times10^{-4}$  Pa. The thicknesses of all layers were controlled by the quartz crystal oscillators. After preparation under a nitrogen atmosphere using epoxy glue and glass slides, all devices were encapsulated immediately. The CIE coordinates and EL spectra were recorded via a PR650 Spectra Scan spectrometer. By utilizing a computer-controlled source meter, the current density-voltage-luminance properties were measured simultaneously.

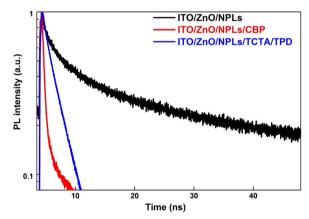


Fig. 7. Time-resolved photoluminescence decay of NPL films contacting different layers. The thicknesses of layers are the same as those in LEDs.

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#### Notes

The authors declare no competing financial interest.

## Appendix A. Supporting information

Supplementary data associated with this article can be found in the online version at http://dx.doi.org/10.1016/j.nanoen.2018.02.004.

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